

Curriculum Vitae

1. Name (Block Letters) : TUSAR KUMAR DASH
2. **Father's/Mother's/ Husband's Name** : Jayaram Dash
3. Current Designation : Assistant Professor (DTE)
4. Department : Electrical Engineering
5. Institute : Jorhat Engineering College
6. Address for correspondence (with pin code): **Electrical Engineering Dept.
Jorhat Engineering College
Jorhat -785007 (Assam)**
7. Permanent address (with pin code) : New Colony, Kamara Sahi
Puri, Odisha-752014
Telephone No : 8763833474
E –mail : tusar.dany@gmail.com

8. Educational qualification (HSLC onwards)

Qualification	Board/University	Year of obtaining degree	Specialization
HSLC	BSE Odisha	2000	All
HS	CHSE Odisha	2002	Science
B.Tech	BPUT Odisha	2007	Electrical Engineering
M.Tech	NIT Rourkela	2013	Power Control and Drives

9. Research Degree(s): NA

Degree	Name of the University	Date of award	Title
Ph.D	-----	-----	-----

10. Details of Teaching/Research/Academic experience:

Designation	Employer	Period of Service		Scale of Pay
		From	To	
Desk Engineer	O.E.G India, Pvt. Ltd.	21 st April 2008	10 th January 2010	Fixed Pay

Assistant Professor	Centurion University, Odisha	13 th June 2013 to 11 th July 2014	6 th Pay
Assistant Professor	NIST, Odisha	15 th July 2014 to 27 th September 2018	6 th Pay
Assistant Professor	JEC, Assam	29 th September 2018 to till date	Fixed pay

11. Details of publication

Journals: NA

Book/e-book/book Chapter: NA

Conferences

1. T.K Dash and B. Chitti Babu, “ Modelling and Control of Single Phase Grid Connected Inverter with Linear and Non-linear loads in DG applications”, National Conference on, “Power Electronics Systems and Applications” PESA-2013 at NIT, ROURKELA.
 2. T.K Dash and B. Chitti Babu, “Modelling of Three Phase Grid Connected Inverter in DG Applications”,IEEE Student’s Conference on Engineering and Systems (SCES-2013) at MNNIT Allahabad.
 3. S. K. Panda and T. K. Dash, "An improved method of frequency detection for grid synchronization of DG systems during grid abnormalities," 2014 International Conference on Circuits, Power and Computing Technologies [ICCPCT-2014], 2014, pp. 153-157, doi: 10.1109/ICCPCT.2014.7054784.
12. Area of Specialization: Power Electronics and Drives
13. Areas of Research Interest: Application of Power Electronics
14. M.Tech Dissertation Supervised: NA

Sl No.	Name of Student	Dissertation	Year	Status
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15. PhD Thesis Supervised: NA

Sl No.	Name of Student	Research Topic	Year of registration	Status
				Completed/Ongoing

16. Professional membership

Sl No.	Details	Organisation	Year
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17. Details of participation in Faculty Development program/STTPs/Training activities

Sl	Details of FDP/STTPs/Training activities	Organisation	No of	Year
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No.	attended		days	
1.	National Workshop on Emerging Trends in Electrical Engineering.	Silicon Institute of Technology, Bhubaneswar	2	2015
2.	Workshop on hybrid power generation.	VSSUT Burla, Odisha	6	2016
3.	FDP- Integration of Smart Grid and RE Sources	AEC Guwahati	5	2018
4.	Digital Pedagogy	IIT Madras	5	2019

18. List of short term courses organised/Conferences arranged/Course modules developed

Sl No.	Details of STTP/conferences organised	Venue	Date
1.	Co-ordinated five day workshop on MS-Excel.	JEC	2/02/2020

19. Chair in Conference/ Invited Lecture/ Key note speaker: NA

20. Details of R&D Projects

Sl No.	Details	Organisation	Year	Status	Amount
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21. Other information:

Following projects are guided by me at UG level

- a) Improvement of voltage profile of a single phase inverter by implementing Average current control technique with different voltage control method.
- b) Modeling and Control of a Single-Phase Grid-Connected Two-Stage Inverter for Battery Application.
- c) Single-Stage Boost Inverter with Coupled Inductor.
- d) Shunt Active filter based on interleaved buck converter.
- e) Anti-wind up scheme for PI and P+ Resonant controller.
- f) Simulation of cascaded Z-Source Multilevel Inverter.
- g) Power factor correction with zeta converter.
- h) Design of PLL.
- i) Design of FLL.
- j) Dual axis solar tracker.